

AMENDMENTS TO THE CLAIMS:

1-2. (Canceled)

3. (Currently amended) A liquid crystal display device, comprising:

pixels equipped with a liquid crystal cell and a switch element, which are arranged at positions where scan lines and data lines intersect, intersect;

a data line drive circuit for supplying, from said data line and said switch element to said liquid crystal cell, a write signal corresponding with image data, data;

a control circuit for inverting a polarity of said write signal after every plurality of scan lines, lines; and

a scan line drive circuit which supplies a drive signal to said scan lines and switches said switch elements ON and OFF while maintaining a constant horizontal scanning period, so that, of the plurality of scan lines to which is supplied a write signal of the a same polarity, in the following scan lines other than those scan lines where the polarity of said write signal is inverted, said drive signal is supplied for a period of time that is shorter, by a predetermined amount of time, than the a time for which said drive signal is supplied to the scan lines where the polarity of said write signal is inverted.

4. (Original) A liquid crystal display device according to claim 3, where said scan line drive circuit adjusts a period for which said drive signal is supplied, in accordance with an output enable signal for controlling whether or not to supply said drive signal to said scan line.

5-6. (Canceled)

7. (New) A liquid crystal display device, comprising:

 a plurality of pixels, a pixel being located where scan lines and data lines intersect, each said pixel comprising a liquid crystal cell and a switching element;

 a data line drive circuit supplying a write signal corresponding with image data;

 a control circuit providing a polarity inversion of said write signal after a predetermined plurality of scan lines; and

 means for supplying a drive signal to said scan lines for switching said switch elements to first and second positions while maintaining a constant horizontal scanning period, so that, of the plurality of scan lines to which is supplied a write signal of a same polarity, in following scan lines other than those scan lines where the polarity of said write signal is inverted, said drive signal is supplied for a period of time that is shorter, by a predetermined amount of time, than a time for which said drive signal is supplied to scan lines where the polarity of said write signal is inverted.

8. (New) A liquid crystal display device according to claim 7, further comprising:

 means to permit a precharging of drain lines for said write signal during those scan lines when the polarity of said write signal is first inverted.

9. (New) A liquid crystal display device according to claim 7, wherein said means for supplying said drive signal provides an output enable signal for controlling whether or not to supply said drive signal to said scan line.

10. (New) A liquid crystal display device according to claim 7, wherein said shorter period is achieved by a correction to a clock signal used to activate gate lines of said switching elements in a scan line.

11. (New) A liquid crystal display device according to claim 7, wherein said means for supplying said drive signal receives an input representing an adjustment for a time delay, said adjustment being selected to reduce a difference of brightness between scan lines.

12. (New) A method to reduce a difference in a brightness between scan lines in a liquid crystal display device having a polarity inversion of write signals after a predetermined plurality of said scan lines, said method comprising:

providing a timing adjustment as an input into a scan line drive circuit that provides a drive signal to said scan lines for switching switch elements to first and second positions in said scan lines while maintaining a constant horizontal scanning period, so that, of the plurality of scan lines to which is supplied a write signal of a same polarity, in following scan lines other than those scan lines where the polarity of said write signal is inverted, said drive signal is supplied for a period of time that is shorter, by a predetermined amount of time, than a time for which said drive signal is supplied to scan lines where the polarity of said write signal is inverted.

13. (New) The method of claim 12, wherein said liquid crystal display device precharges drain lines for said write signal as an initial operation for those scan lines in which the polarity of said write signal is first inverted.

14. (New) The method of claim 12, wherein said drive signal provides an output enable signal for controlling whether or not to supply said drive signal to said scan line.

15. (New) The method of claim 12, wherein said shorter period is achieved by a correction to a clock signal used to activate gate lines of said switching elements in a scan line.